

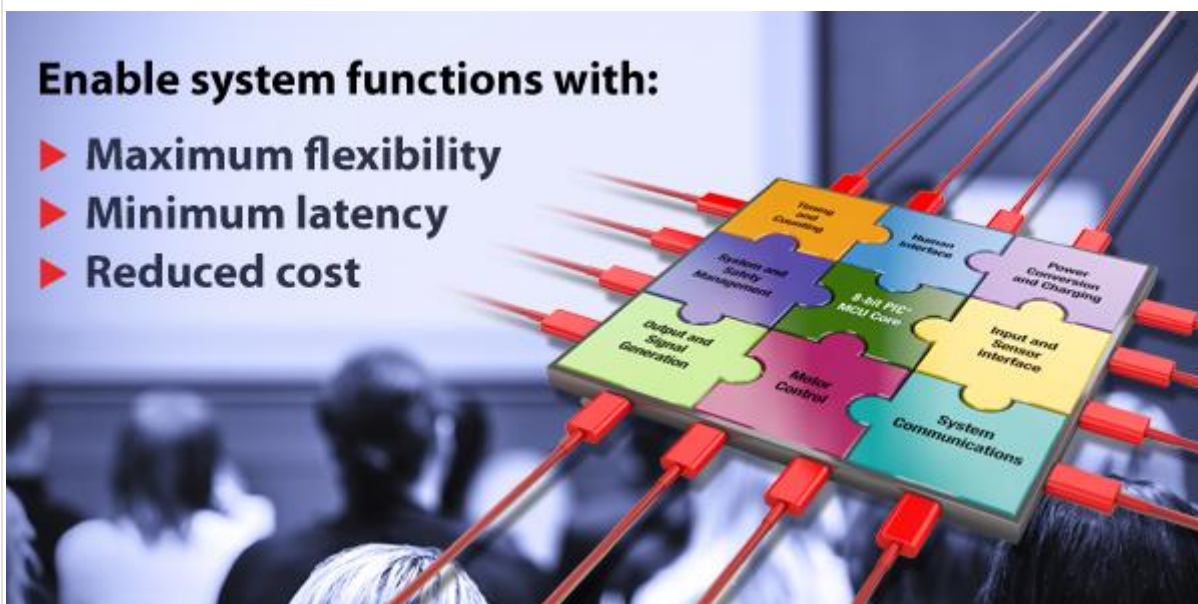


Join us for a **FREE** seminar

Learn How to Reduce Cost and Development Time with PIC® MCUs

Enable system functions with:

- ▶ Maximum flexibility
- ▶ Minimum latency
- ▶ Reduced cost



Join Us on Tuesday, 10th November

Design With Core Independent Peripherals

PIC® Microcontrollers with Core Independent Peripherals are designed to support the way you design your products. By organizing an end application into its basic functional components, we were able to design flexible hardware peripherals that can easily support a wide variety of functions.

Each peripheral has been crafted to work both individually and in tandem with other parts of a PIC® MCU to enable commonly used tasks with minimal supervision from the core processor. This allows a PIC® MCU to support as many system functions as possible, while minimizing the use of external components and the need for power-hungry resources. Our function enablement approach helps reduce system cost and can increase an end product's performance and robustness.

And Learn How To:

- Build applications you never thought were possible with a low-cost MCU
- Save money by using the core-independent approach to embedded design
- Lower your overall system power consumption using flexible hardware peripherals
- Decrease your development time with new hardware and software tools from Microchip

Learn more at www.microchip.com/CIP

Click here to download our [8-bit Quick Reference Sheet](#)

Location:



Tuesday, 10th November
Sofia,
47, Obikolna str.,
1582, Bulgaria
Tel: 00359 2 9155831

Register Today:

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Book early to avoid disappointment!

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