

Design Idea DI-63

TinySwitch-II® 4.8 W CV/CC Charger with Output Cable-Drop Compensation



Application	Device	Power Output	Input Voltage	Output Voltage	Topology
Charger	TNY266P	4.8 W	85-265 VAC	6 V, 800 mA $\pm 7\%$	Flyback

Design Highlights

- Universal input, no-load consumption < 300 mW
- Meets CISPR-22 Class B EMI without a Y capacitor
- Extremely simple circuit, requires only 32 components!
- Circuit has output cable voltage drop compensation
- Ultra-low Leakage current: < 5 μA at 265 VAC input

Operation

Fusible resistor RF1 provides short-circuit fault protection and limits start-up inrush current. Inductors L1 and L2 and capacitors C1 and C2 form a low-cost pi (π) filter that attenuates conducted EMI.

Transformer (T1) has two shield windings (1-3 and 1-open) which reduce the generation of EMI noise. Winding phasing and D7 orientation let no secondary winding current flow when the MOSFET in U1 is ON, so primary winding current stores energy in the core of T1. When the MOSFET in U1 turns OFF, the energy stored in T1 drives current out of the secondary

winding, forward biasing D7, charging C7 and developing/maintaining the output voltage across C7. The V_{BE} of Q1 and the V_Z of VR1 determine the CV set point. The voltage across R4, R5 and the U2-LED determine the CC set point.

Resistors R7, R10 and diode D6 compensate for output-cable voltage-drop. The CV portion of the output VI curve is flat, at the end of the cable, because a current sense resistor (R10) equal to the DC resistance of the output cable is in the voltage feedback loop. Diode D6 implements temperature compensation for the cable drop.

The combination of *TinySwitch-II* frequency jitter, the output diode snubber (R6 and C5), the T1 shield windings and careful primary clamp circuit component selection enable compliance with CISPR-22, Class B conducted EMI limits, without a Y-1 Safety capacitor. Eliminating the Y capacitor gives this circuit very low (< 5 μA) AC leakage current.

This circuit is suitable for portable electronics chargers.

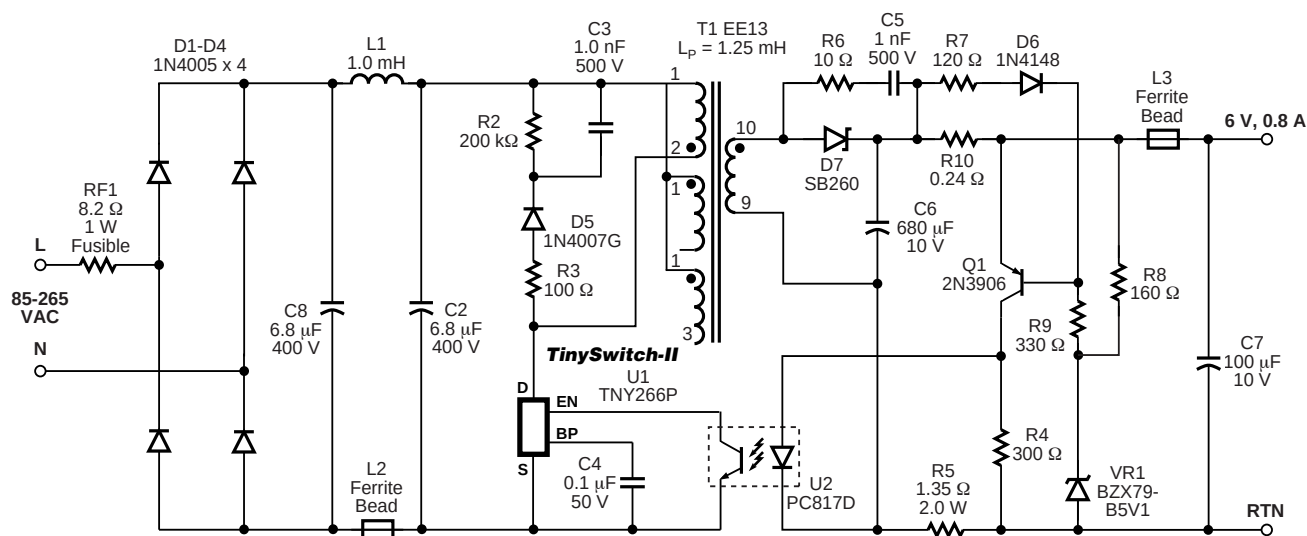


Figure 1. TinySwitch-II Based Charger/Adapter Circuit Diagram.

PI-3704-091903

Key Design Points

- Secondary feedback bias current and output tolerances are minimized by using a low current part for VR1
- Primary clamp losses are minimized by keeping the voltage reflected across the transformer (V_{OR}) low
- Picking wire sizes that fill each winding layer produce transformers with the lowest leakage inductance
- Putting the floating shield winding between the primary and secondary windings reduces EMI noise
- A high gain opto-coupler (CTR = 200-400) keeps the CC portion of the output curve more vertical
- R10 should equal the output cable resistance value

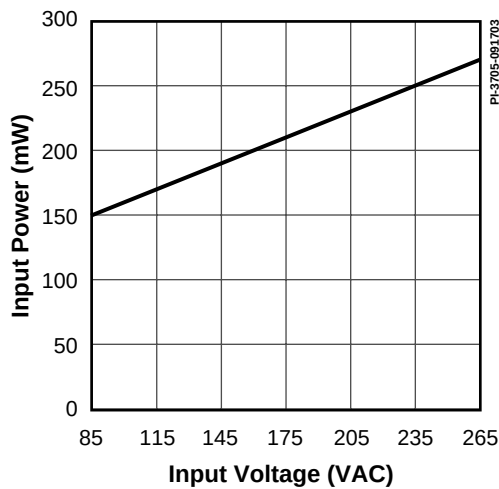


Figure 2. No-Load Input Power vs. Line Voltage.

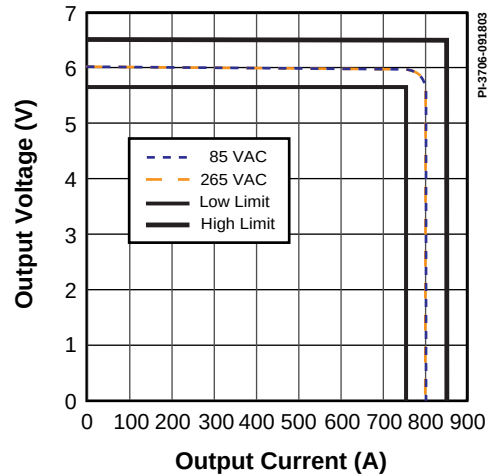


Figure 3. 6.0 VDC, 800 mA CV/CC Curve.

TRANSFORMER PARAMETERS	
Core Material	EE16, TDK PC40, or equivalent A_L of 127.5 nH/T ²
Bobbin	EE16, 10 pin
Winding Order (pin numbers)	Core shield (1-3), tape Primary (1-2), tape Shield (1-floating), tape Secondary (10-9), tape
Primary Inductance	1.25 mH $\pm 10\%$
Primary Resonant Frequency	500 kHz (minimum)
Leakage Inductance	35 μ H (maximum)

Table 1. Transformer Construction Information.

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