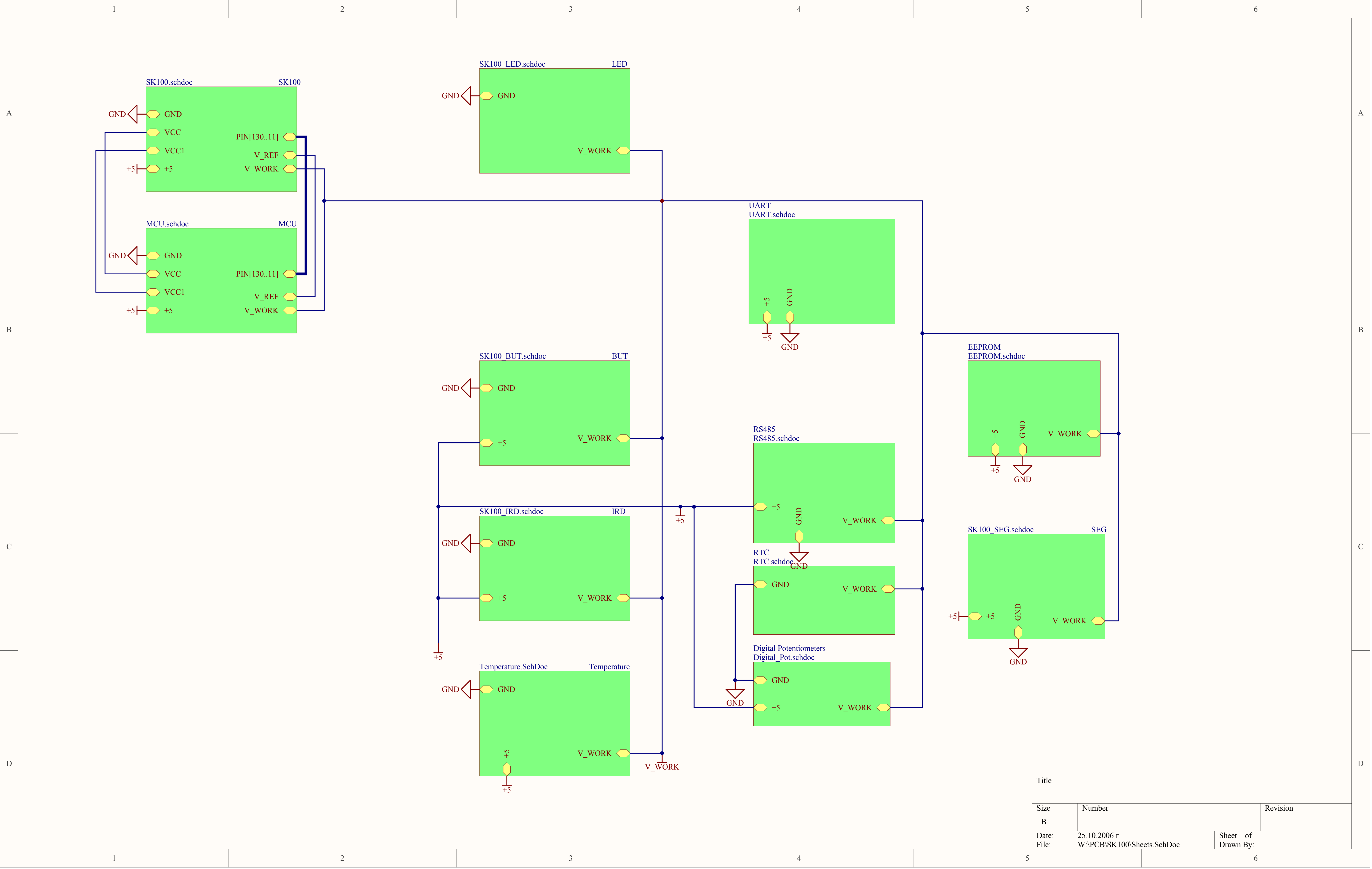
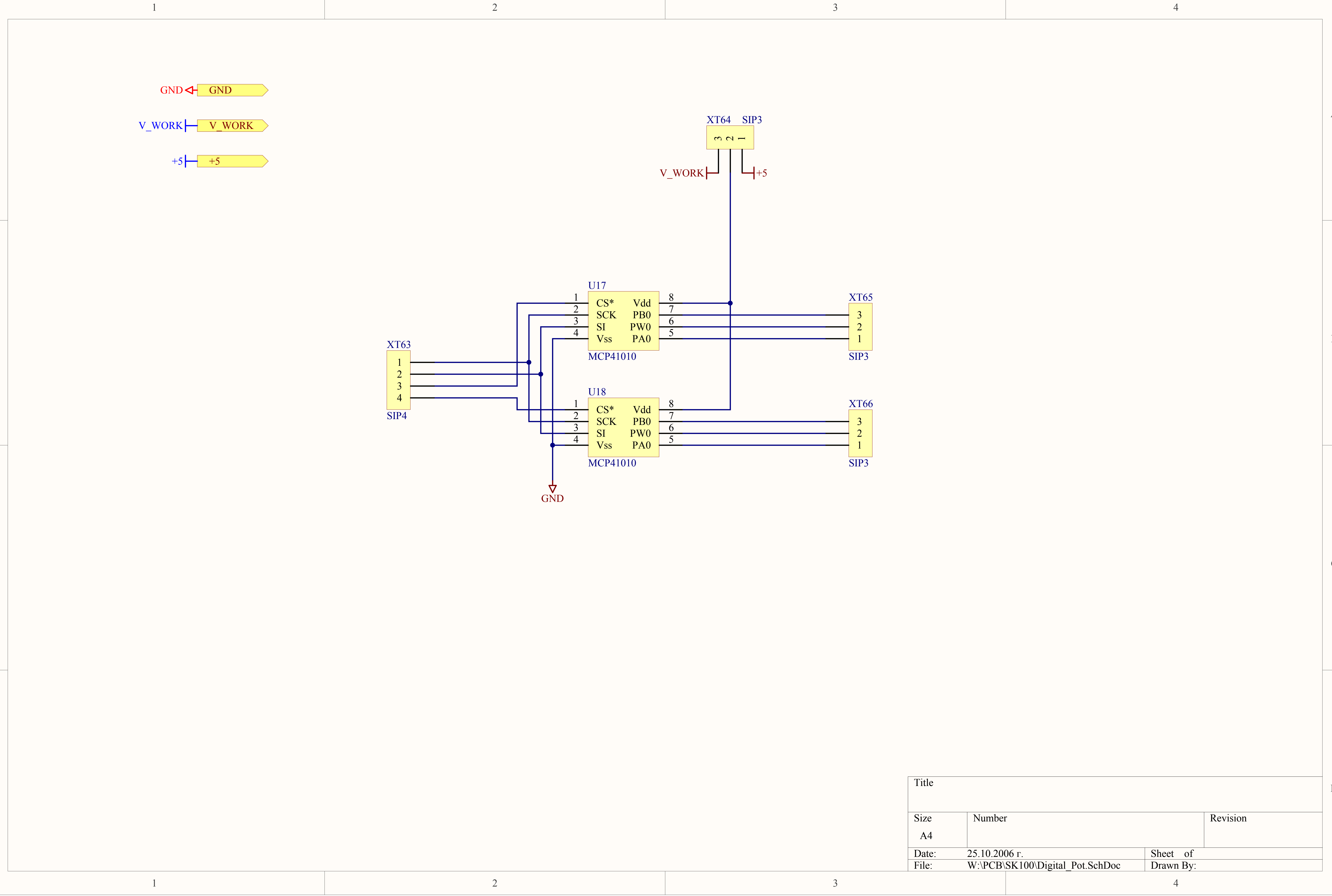
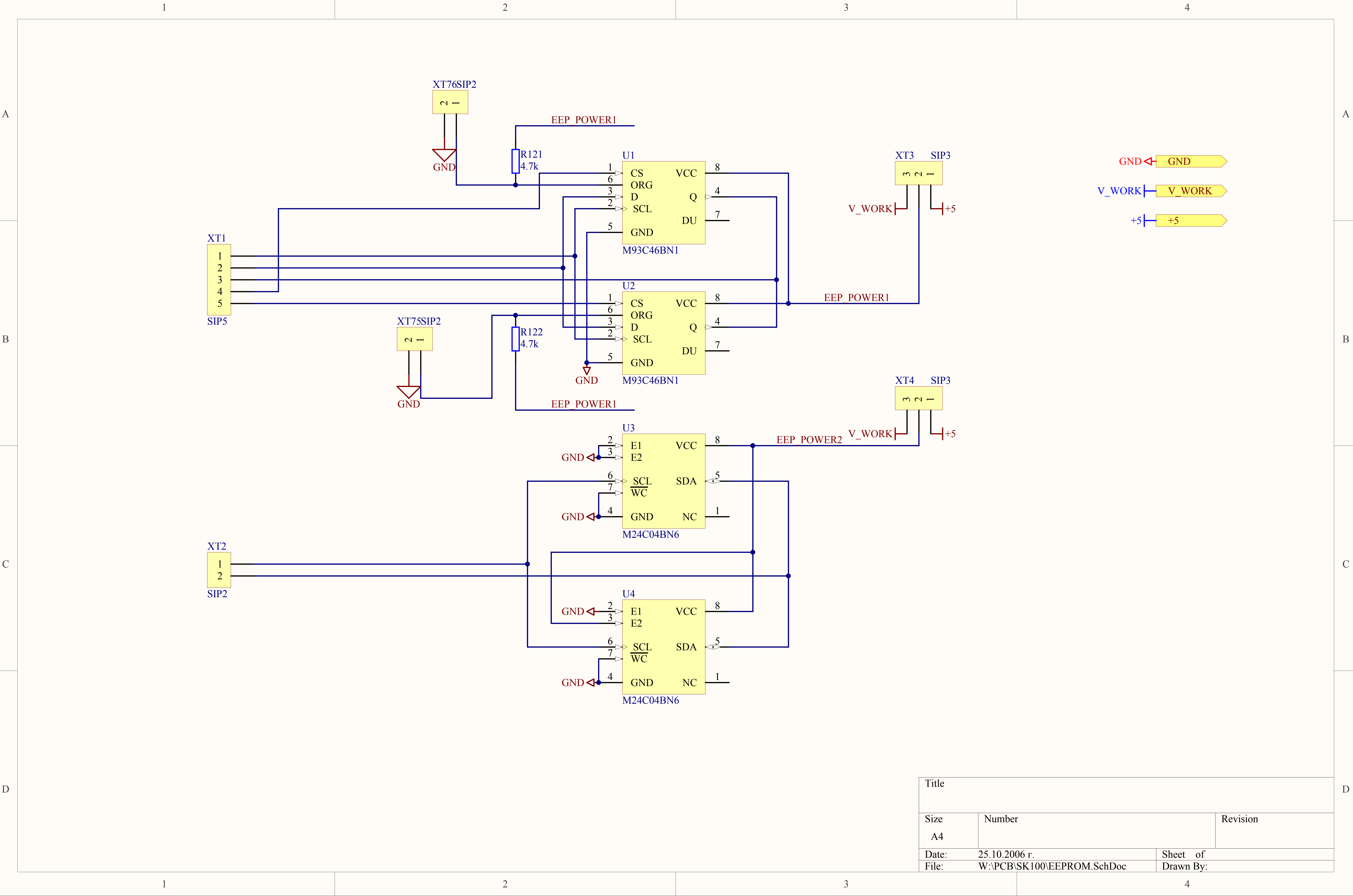




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Title		
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Date:	25.10.2006 r.	Sheet of
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Title		
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Date:	25.10.2006 r.	Sheet of
File:	W:\PCB\SK100\EEPROM.SchDoc	Drawn By:

A

A

B

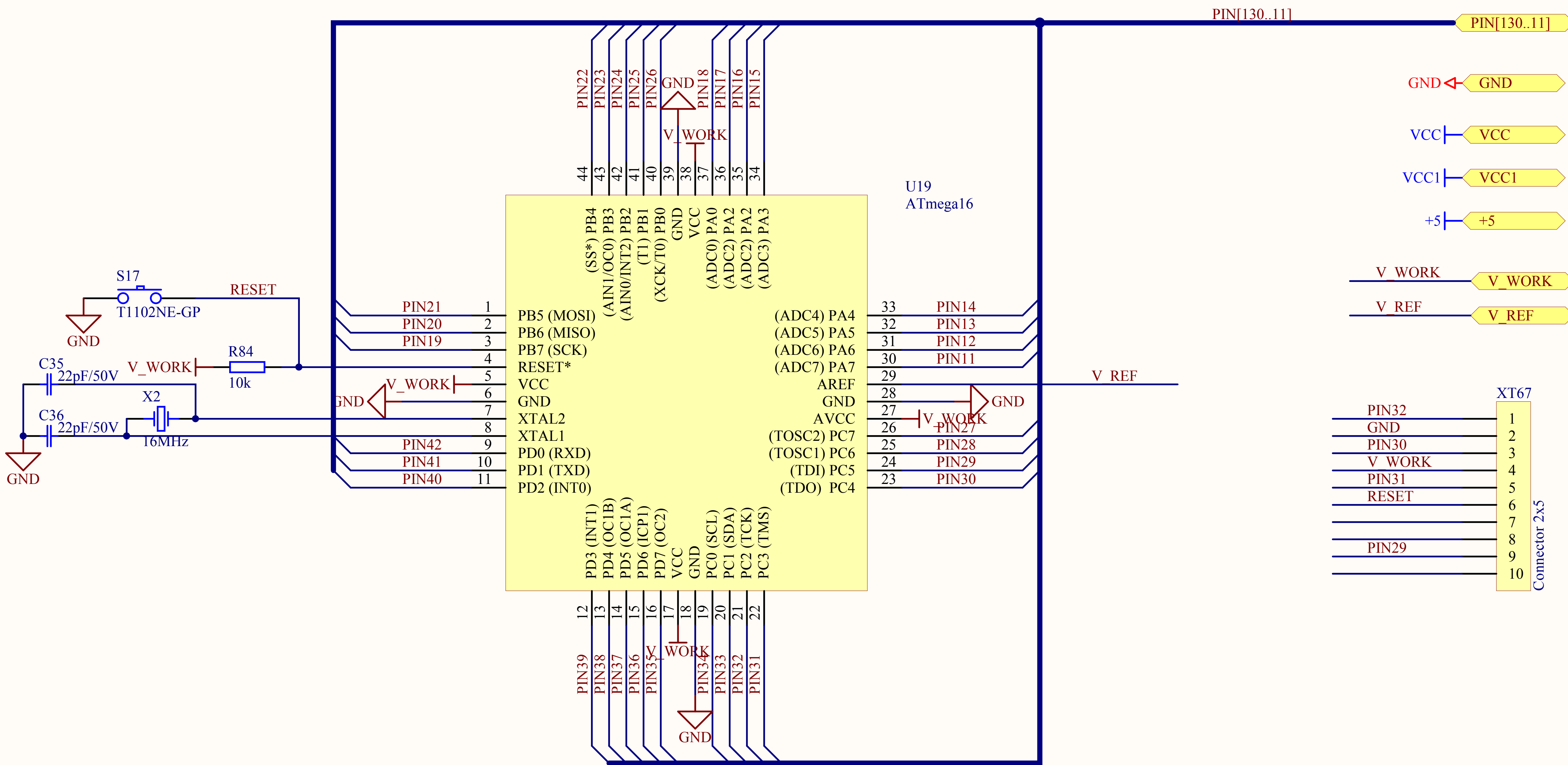
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C

C

D

D



Title		
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A

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C

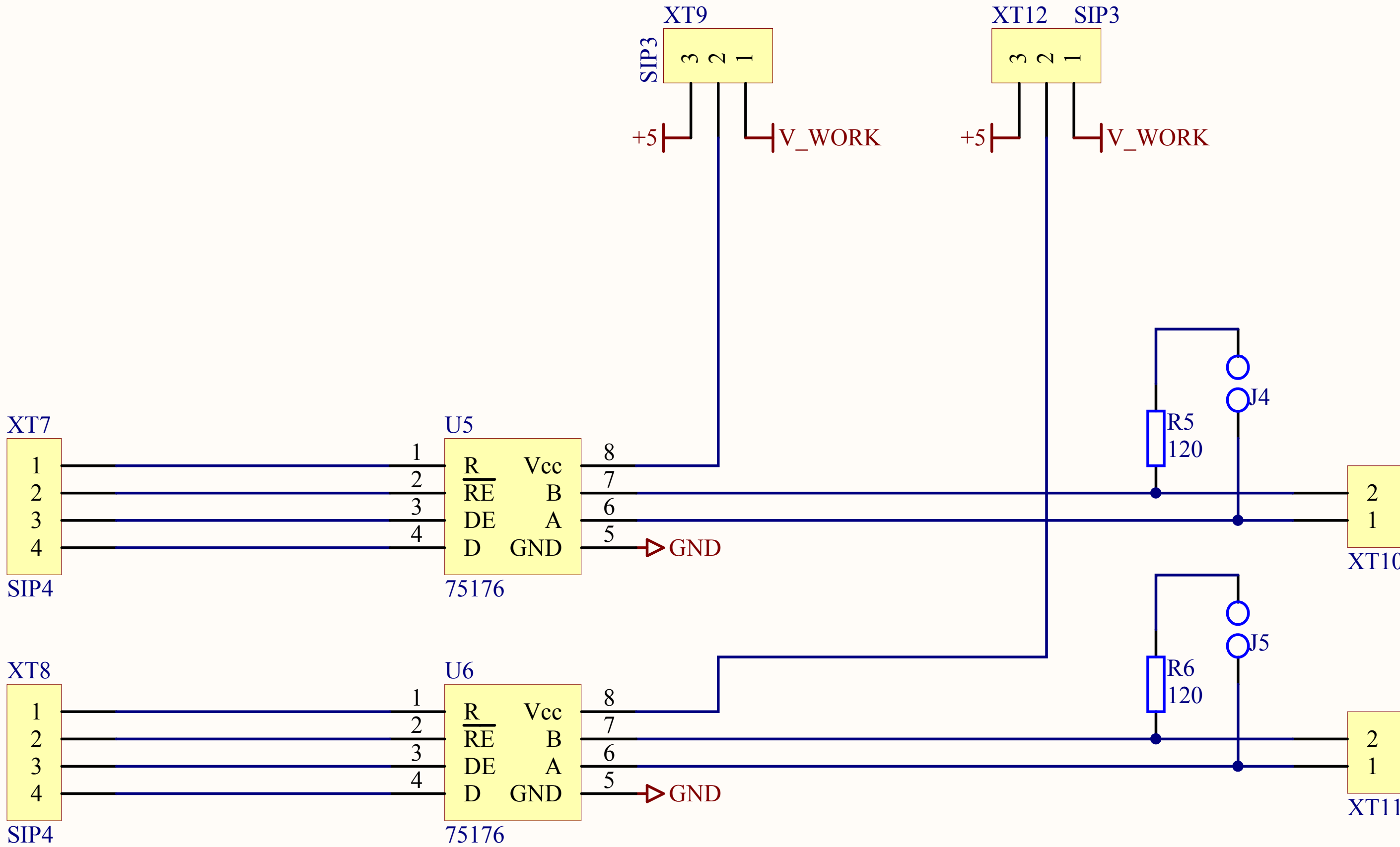
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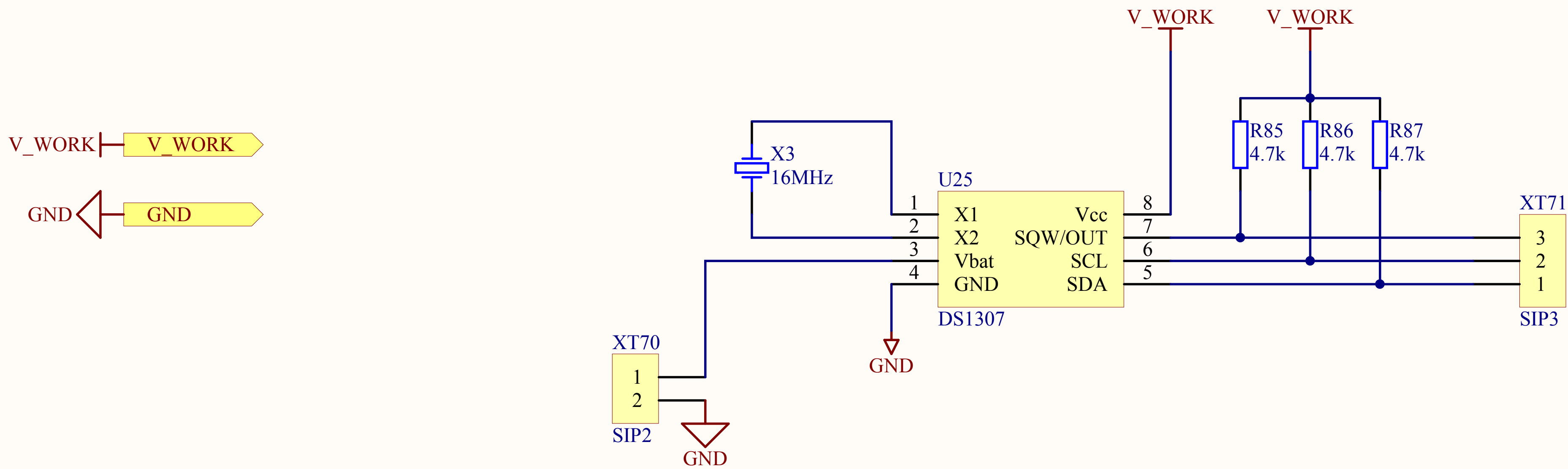
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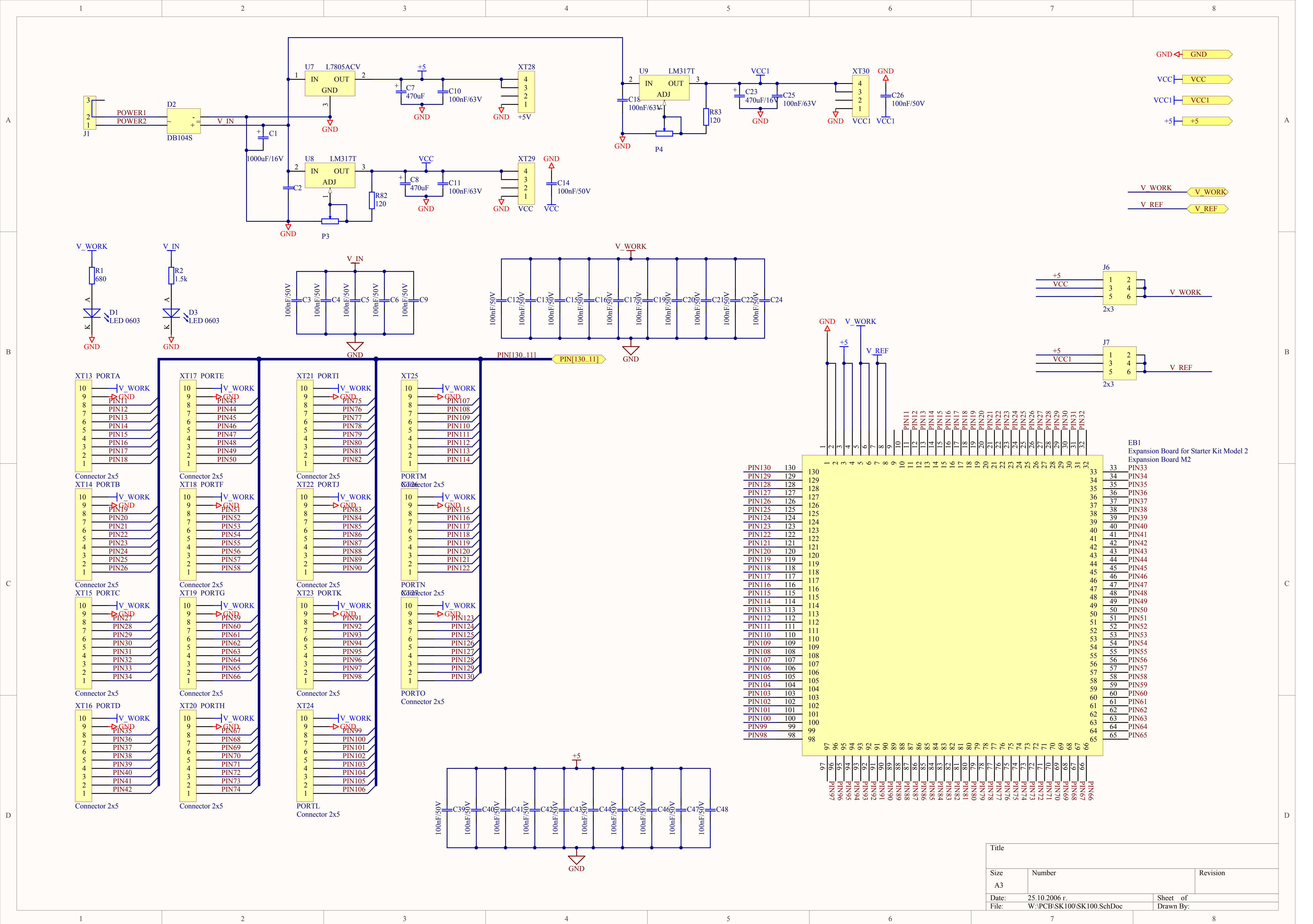
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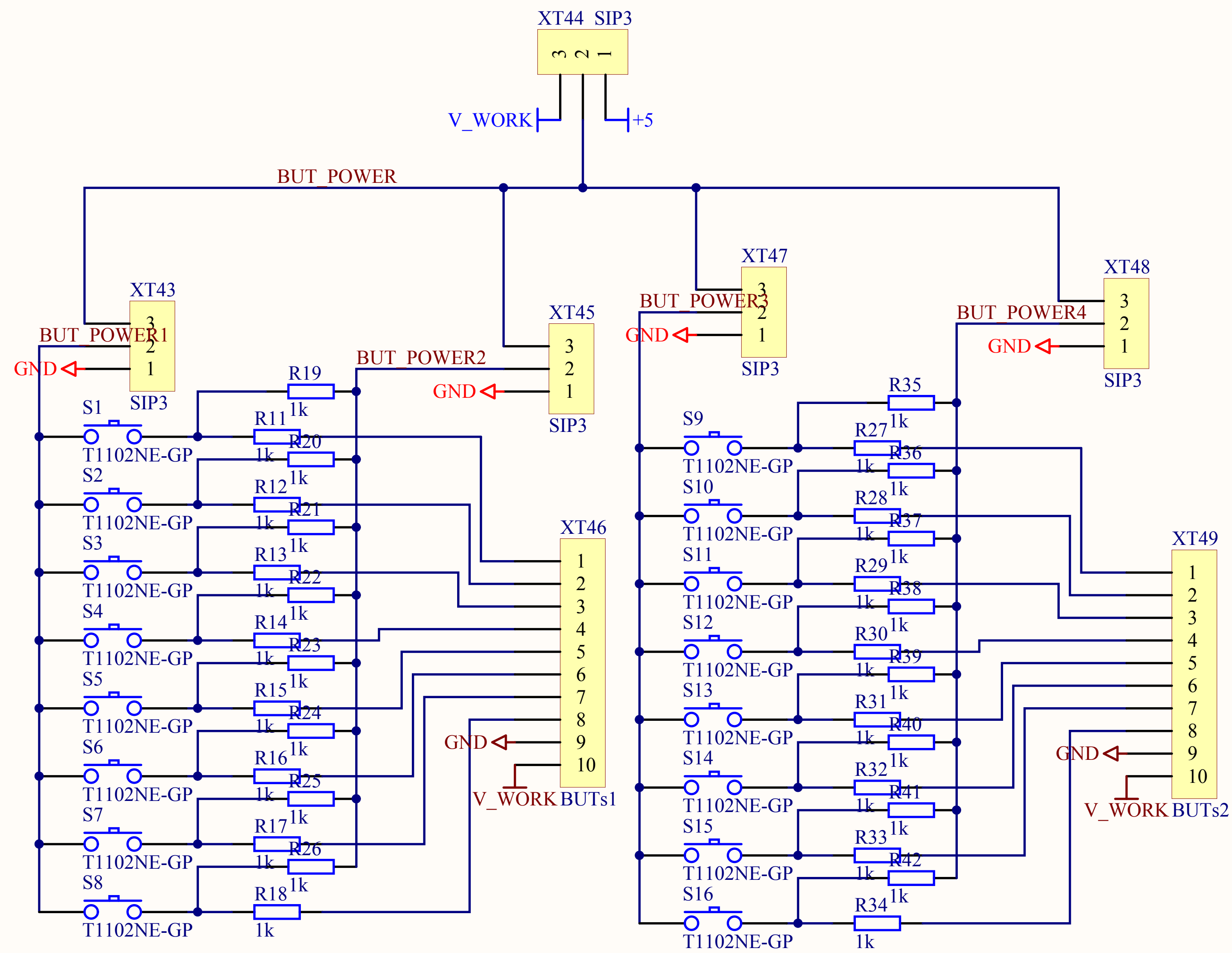
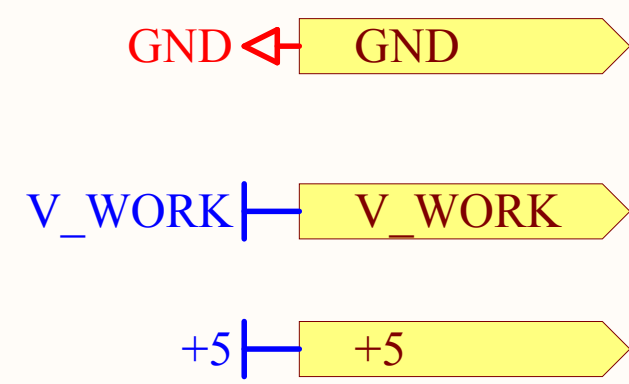


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Title		
Size	Number	Revision
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Date:	25.10.2006 r.	Sheet of
File:	W:\PCB\SK100\RTC.SchDoc	Drawn By:





Title		
Size	Number	Revision
A4		
Date:	25.10.2006 r.	Sheet of
File:	W:\PCB\SK100\SK100_BUT.SchDoc	Drawn By:

A

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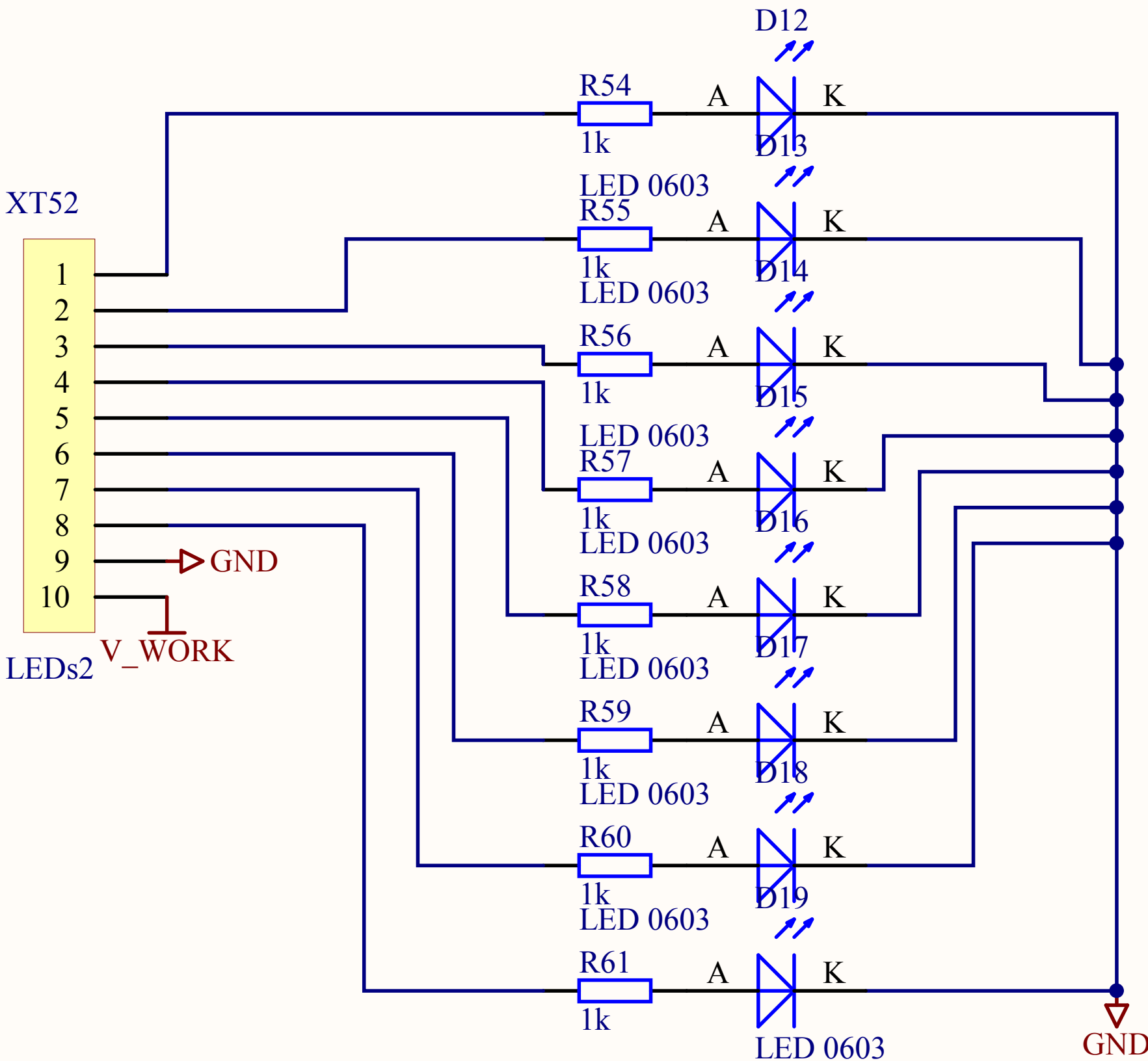
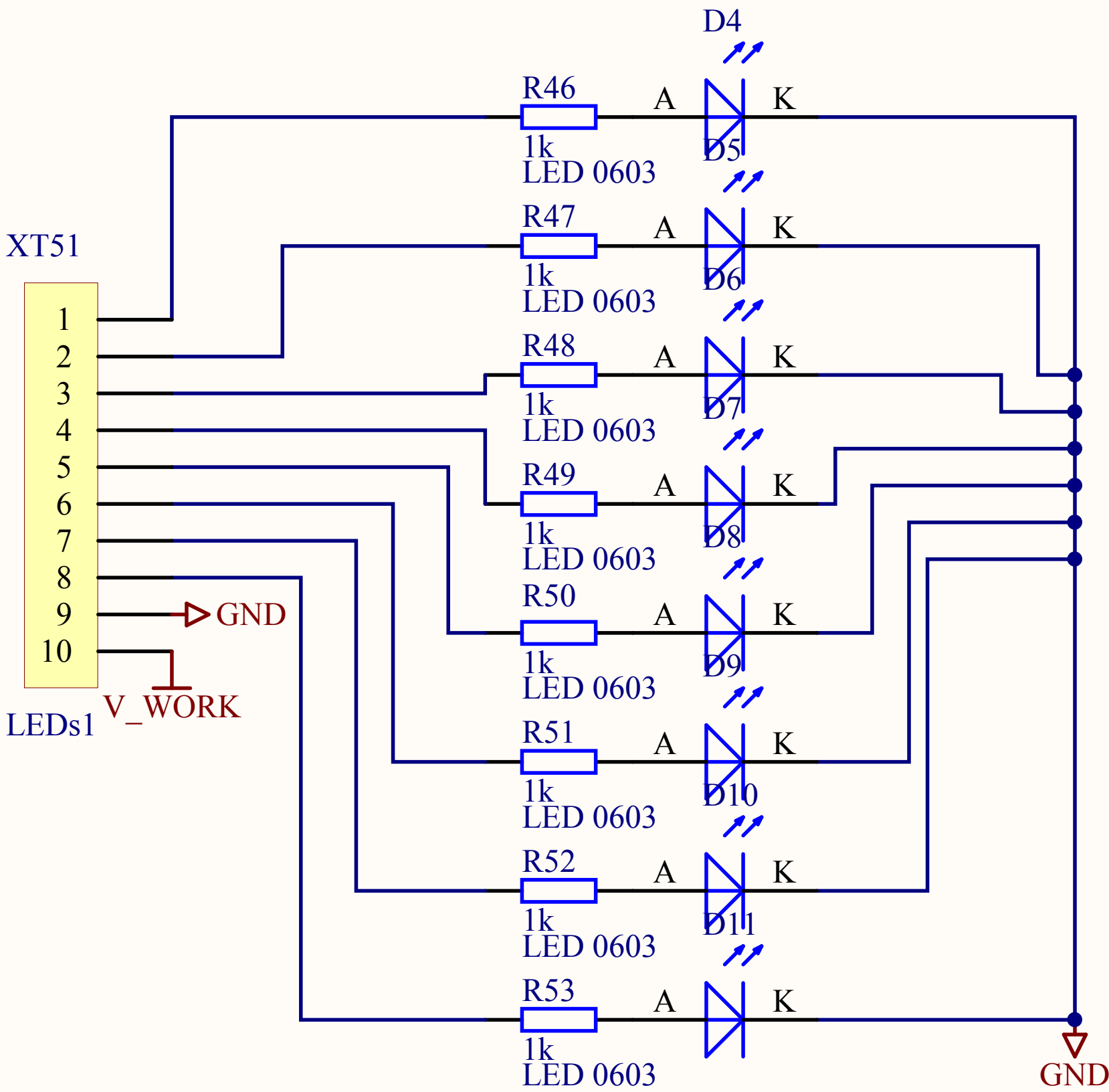
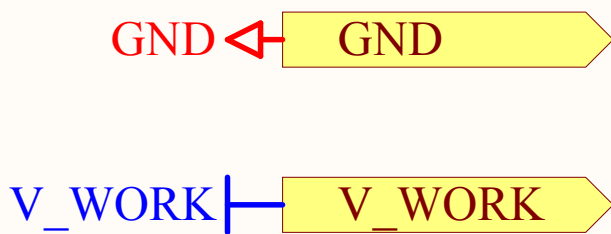
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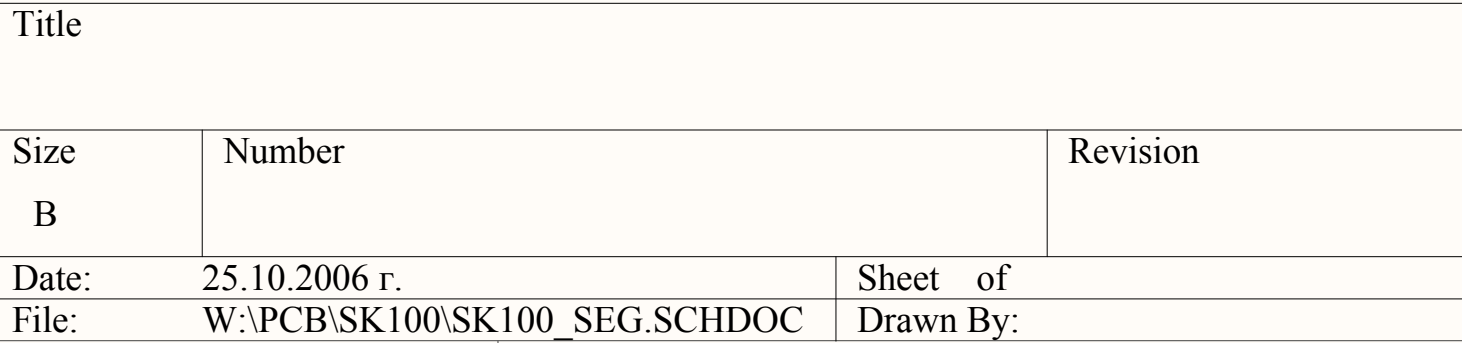
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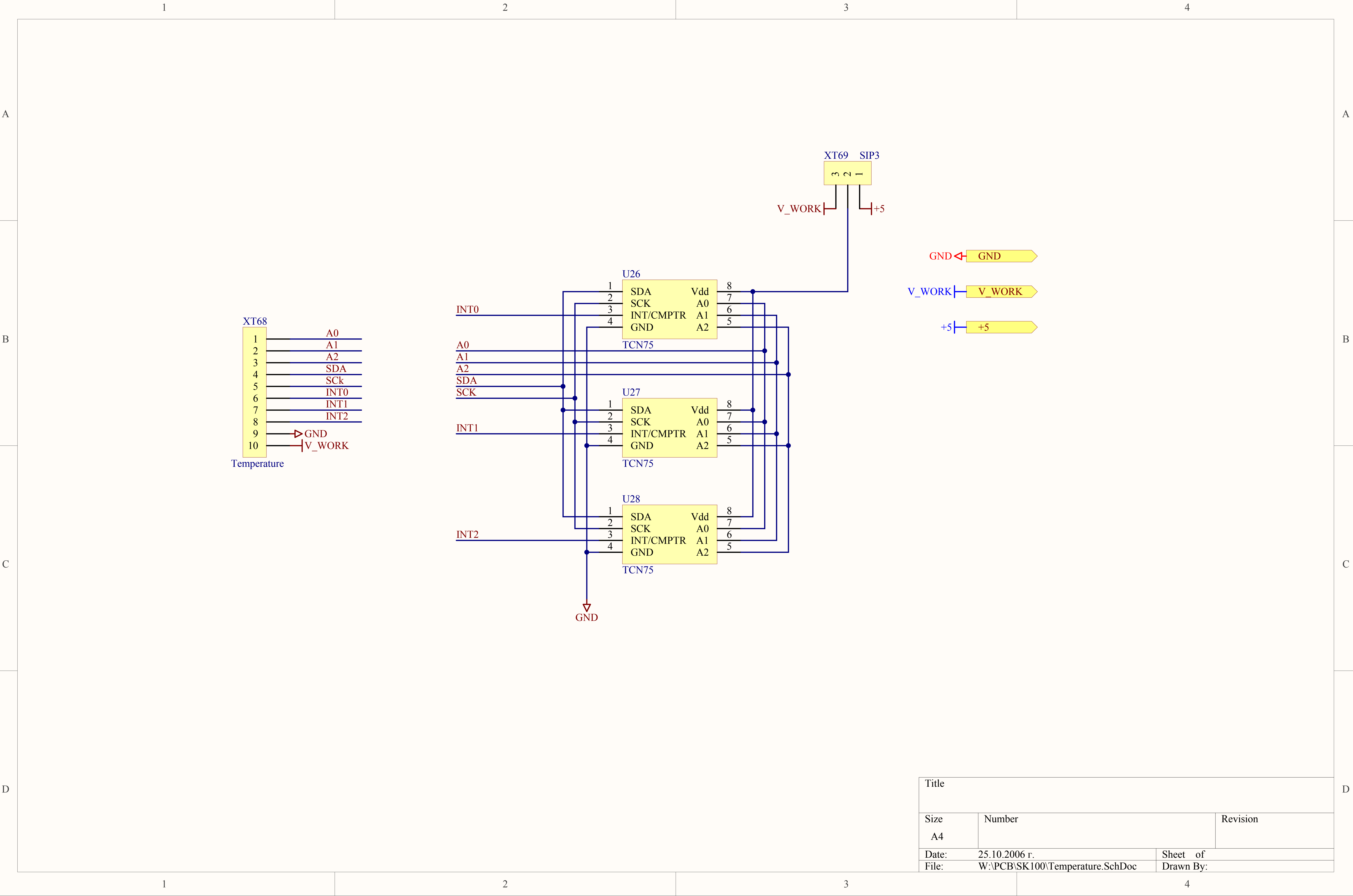
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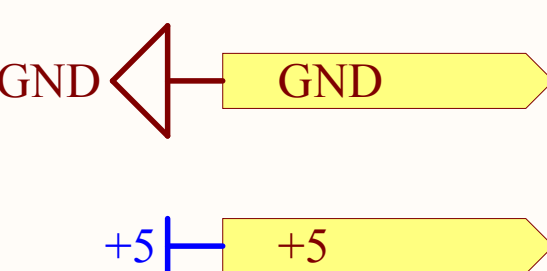
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Title		
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R1 D1
R2 D3

+5U
UCC1
UCC

C10 C6
C7 U7

R82

P3

C11

R83

P4

C4

U8

U9

R45

R44

R43

XT12

XT50

RS-485

U6

R6

XT11

XT9

J5

RS-485

U5

R5

XT10

J4

C47

C46

R61

D18

D19

S15

R34

S16

S14

R33

S13

R32

S12

R31

S11

R30

S10

R29

S9

R28

S8

R27

S7

R26

S6

R25

S5

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R-114

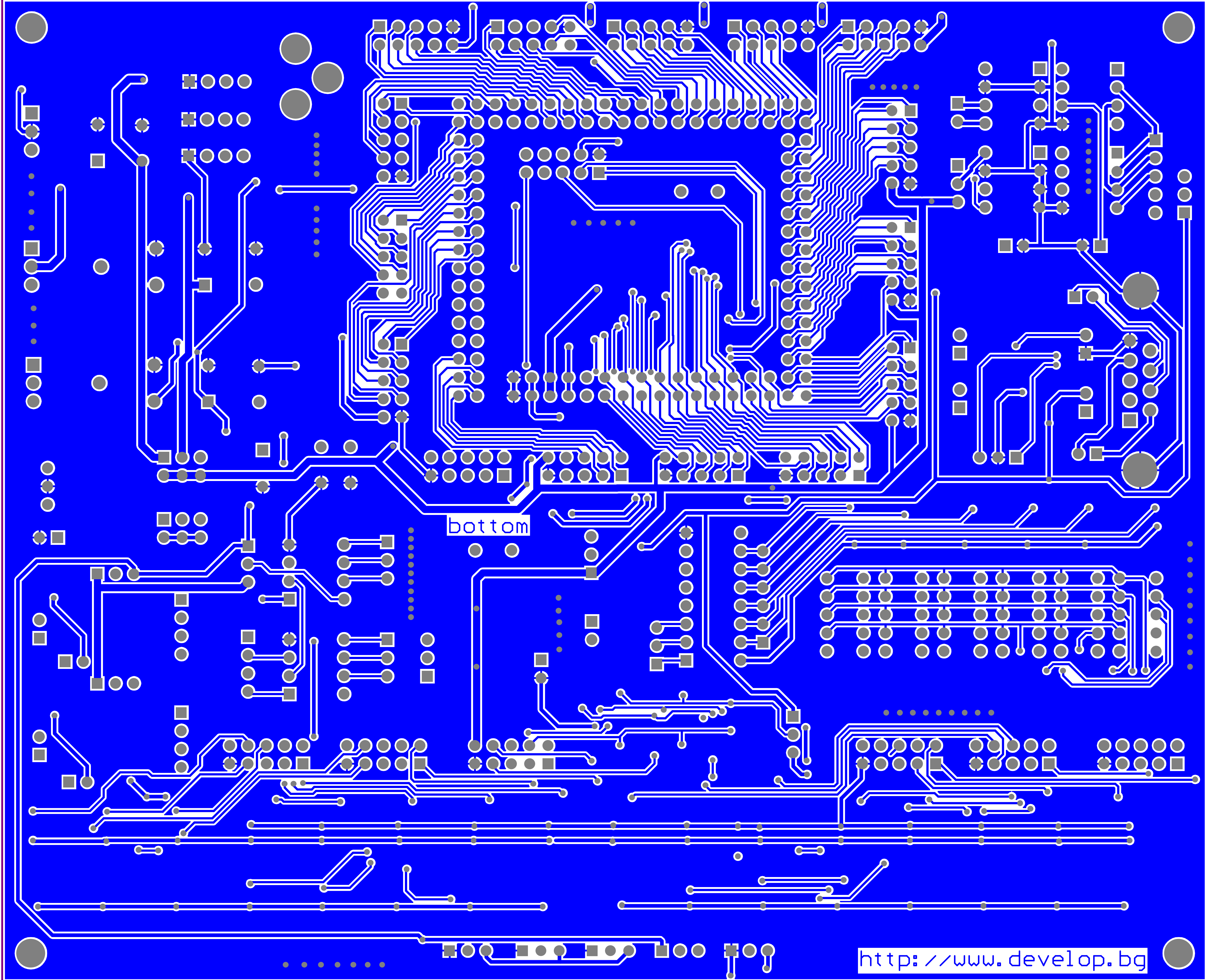
S1

R-115

S1

R-116

S1



bottom

