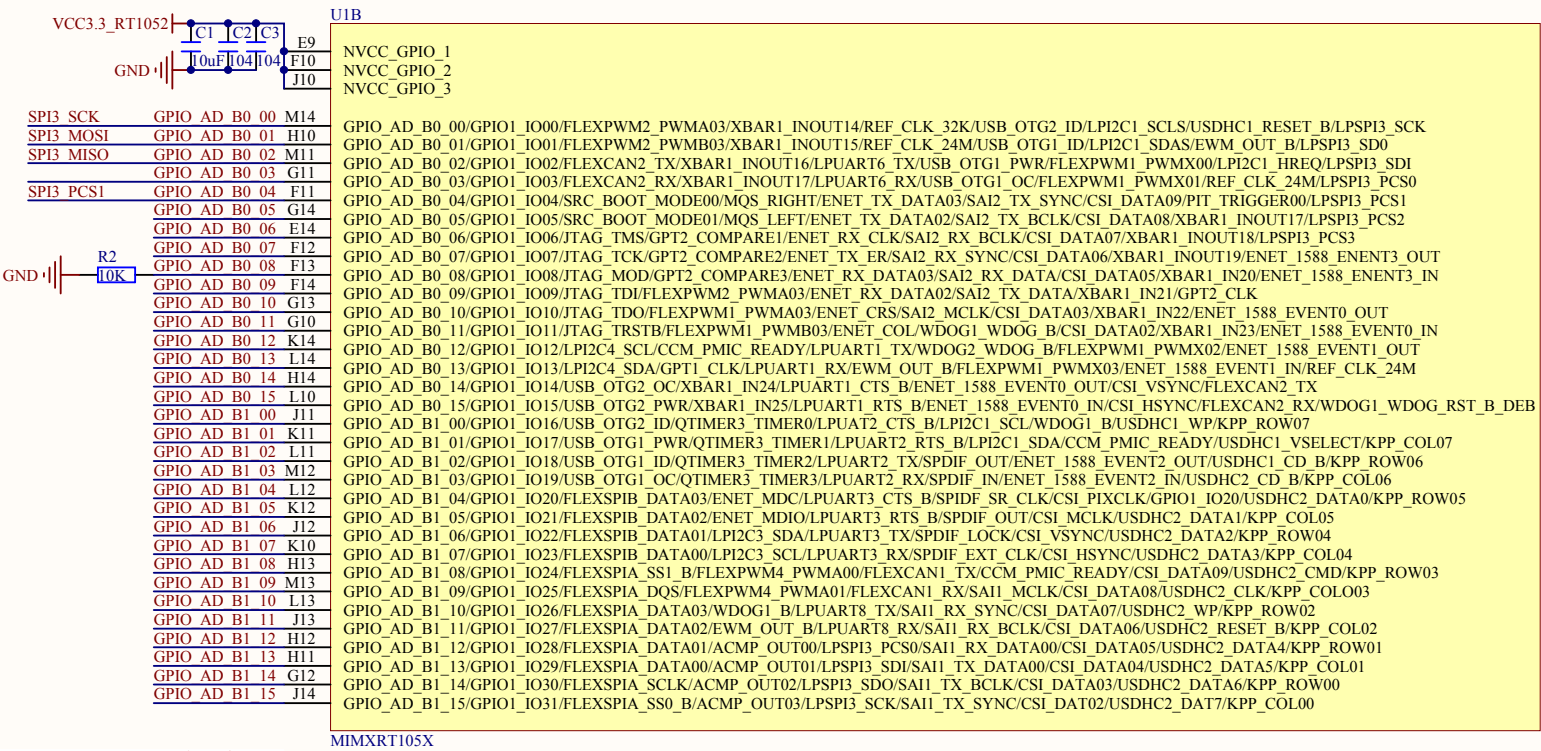
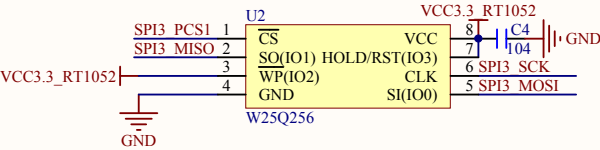


GPIO1



JTAG_MOD脚,即GPIO AD B0_08(GPIO1 IO08)加一个下拉电阻,选择SWD模式进行仿真调试和下载。
如果JTAG_MOD脚接一个上拉电阻,则选择JTAG模式进行仿真调试和下载。
但是JTAG模式占用IO多,所以,我们选择SWD模式。

SPI FLASH



Title:		
LMX RT1052 Core Board		
Author:	Size:	
	A4	
Date:	File:	SchDoc
2018-05-01	RT1052_CORE_GPIO1&FLASH.SchDoc	
Revision:	Version:	
V1.4	V1.4	

GPIO2

UIE

GPIO_B0_00 D7	GPIO_B0_00/GPIO2_I000/LCD_CLK/QTIMER1_TIMER0/MQS_RIGHT/LPSPI4_PCS0/FLEXIO2_FLEXIO00/SEMC_CSX01
GPIO_B0_01 E7	GPIO_B0_01/GPIO2_I001/LCD_ENABLE/QTIMER1_TIMER1/MQS_LEFT/LPSPI4_SDI/FLEXIO2_FLEXIO01/SEMC_CSX02
GPIO_B0_02 E8	GPIO_B0_02/GPIO2_I002/LCD_HSYNC/QTIMER1_TIMER2/FLEXCAN1_TX/LPSPI4_SDO/FLEXIO2_FLEXIO02/SEMC_CSX03
GPIO_B0_03 D8	GPIO_B0_03/GPIO2_I003/LCD_VSYNC/QTIMER2_TIMER0/FLEXCAN1_RX/LPSPI4_SCK/FLEXIO2_FLEXIO03/WDOG2_RESET_B_DEB
GPIO_B0_04 C8	GPIO_B0_04/GPIO2_I004/LCD_DATA00/QTIMER2_TIMER1/LPI2C2_SCL/FLEXIO2_FLEXIO04/SRC_BOOT_CFG00
GPIO_B0_05 B8	GPIO_B0_05/GPIO2_I005/LCD_DATA01/QTIMER2_TIMER2/LPI2C2_SDA/FLEXIO2_FLEXIO05/SRC_BOOT_CFG01
GPIO_B0_06 A8	GPIO_B0_06/GPIO2_I006/LCD_DATA02/QTIMER3_TIMER0/FLEXPWM2_OWMA00/FLEXIO2_FLEXIO06/SRC_BOOT_CFG02
GPIO_B0_07 A9	GPIO_B0_07/GPIO2_I007/LCD_DATA03/QTIMER3_TIMER1/FLEXPWM2_PWMB00/FLEXIO2_FLEXIO07/SRC_BOOT_CFG03
GPIO_B0_08 B9	GPIO_B0_08/GPIO2_I008/LCD_DATA04/QTIMER3_TIMER2/FLEXPWM2_PWMA01/LPUART3_TX/FLEXIO2_FLEXIO08/SRC_BOOT_CFG04
GPIO_B0_09 C9	GPIO_B0_09/GPIO2_I009/LCD_DATA05/QTIMER4_TIMER0/FLEXPWM2_PWMB01/LPUART3_RX/FLEXIO2_FLEXIO09/SRC_BOOT_CFG05
GPIO_B0_10 D9	GPIO_B0_10/GPIO2_I010/LCD_DATA06/QTIMER4_TIMER1/FLEXPWM2_PWMA02/SAI1_TX_DATA03/FLEXIO2_FLEXIO10/SRC_BOOT_CFG06
GPIO_B0_11 A10	GPIO_B0_11/GPIO2_I011/LCD_DATA07/QTIMER4_TIMER2/FLEXPWM2_PWMB02/SAI1_TX_DATA02/FLEXIO2_FLEXIO11/SRC_BOOT_CFG07
GPIO_B0_12 C10	GPIO_B0_12/GPIO2_I012/LCD_DATA08/XBAR1_INOUT10/SAI1_TX_DATA01/FLEXIO2_FLEXIO12/SRC_BOOT_CFG08
GPIO_B0_13 D10	GPIO_B0_13/GPIO2_I013/LCD_DATA09/XBAR1_INOUT11/SAI1_MCLK/FLEXIO2_FLEXIO13/SRC_BOOT_CFG09
GPIO_B0_14 E10	GPIO_B0_14/GPIO2_I014/LCD_DATA10/XBAR1_INOUT12/ARM_CM7_TXEV/SAI1_RX_SYNC/FLEXIO2_FLEXIO14/SRC_BOOT_CFG10
GPIO_B0_15 E11	GPIO_B0_15/GPIO2_I015/LCD_DATA11/XBAR1_INOUT13/ARM_CM7_RXEV/SAI1_RX_BCLK/FLEXIO2_FLEXIO15/SRC_BOOT_CFG11
GPIO_B1_00 A11	GPIO_B1_00/GPIO2_I016/LCD_DATA12/XBAR1_INOUT14/LPUART4_TX/SAI1_RX_DATA00/FLEXIO2_FLEXIO16/FLEXPWM1_PWMA03
GPIO_B1_01 B11	GPIO_B1_01/GPIO2_I017/LCD_DATA13/XBAR1_INOUT15/LPUART4_RX/SAI1_TX_DATA00/FLEXIO2_FLEXIO17/FLEXPWM1_PWMB03
GPIO_B1_02 C11	GPIO_B1_02/GPIO2_I018/LCD_DATA14/XBAR1_INOUT16/LPSPI4_PCS2/SAI1_TX_BCLK/FLEXIO2_FLEXIO18/FLEXPWM2_PWMA03
GPIO_B1_03 D11	GPIO_B1_03/GPIO2_I019/LCD_DATA15/XBAR1_INOUT17/LPSPI4_PCS1/SAI1_TX_SYNC/FLEXIO2_FLEXIO19/FLEXPWM2_PWMB03
GPIO_B1_04 E12	GPIO_B1_04/GPIO2_I020/LCD_DATA16/LPSPI4_PCS0/CSI_DATA15/ENET_RX_DATA00/FLEXIO2_FLEXIO20
GPIO_B1_05 D12	GPIO_B1_05/GPIO2_I021/LCD_DATA17/LPSPI4_SDI/CSI_DATA14/ENET_RX_DATA01/FLEXIO2_FLEXIO21
GPIO_B1_06 C12	GPIO_B1_06/GPIO2_I022/LCD_DATA18/LPSPI4_SDO/CSI_DATA13/ENET_RX_EN/FLEXIO2_FLEXIO22
GPIO_B1_07 B12	GPIO_B1_07/GPIO2_I023/LCD_DATA19/LPSPI4_SCK/CSI_DATA12/ENET_EX_DATA00/FLEXIO2_FLEXIO23
GPIO_B1_08 A12	GPIO_B1_08/GPIO2_I024/LCD_DATA20/QTIMER1_TIMER3/CSI_DATA11/ENET_TX_DATA01/FLEXIO2_FLEXIO24/FLEXCAN2_TX
GPIO_B1_09 A13	GPIO_B1_09/GPIO2_I025/LCD_DATA21/QTIMER2_TIMER3/CSI_DATA10/ENET_TX_EN/FLEXIO2_FLEXIO25/FLEXCAN2_RX
GPIO_B1_10 B13	GPIO_B1_10/GPIO2_I026/LCD_DATA22/QTIMER3_TIMER3/CSI_DATA00/ENET_TX_CLK/FLEXIO2_FLEXIO26/ENET_REF_CLK
GPIO_B1_11 C13	GPIO_B1_11/GPIO2_I027/LCD_DATA23/QTIMER4_TIMER3/CSI_DATA01/ENET_RX_ER/FLEXIO2_FLEXIO27/LPSPI4_PCS3
GPIO_B1_12 D13	GPIO_B1_12/GPIO2_I028/LPUART5_TX/CSI_PIXCLK/ENET_1588_EVENT0_IN/FLEXIO2_FLEXIO28/USDHC1_CD_B
GPIO_B1_13 D14	GPIO_B1_13/GPIO2_I029/WDOG1_B/LPUART5_RX/CSI_VSYNC/ENET_1588_EVENT0_OUT/FLEXIO2_FLEXIO29/USDHC1_WP
GPIO_B1_14 C14	GPIO_B1_14/GPIO2_I030/ENET_MDC/FLEXPWM4_PWMA02/CSI_HSYNC/XBAR1_IN02/FLEXIO2_FLEXIO30/USDHC1_VSELECT
GPIO_B1_15 B14	GPIO_B1_15/GPIO2_I031/ENET_MDIO/FLEXPWM4_PWMA03/CSI_MCLK/XBAR1_IN03/FLEXIO2_FLEXIO31/USDHC1_RESET_B

MIMXRT105X

PIN OUT

JP4			JP2		
34	34	GPIO AD B1_15	94	94	GND
33	33	GPIO AD B1_11	93	93	GPIO EMC 22
32	32	GPIO AD B1_13	92	92	GPIO EMC 20
31	31	GPIO AD B1_07	91	91	GPIO EMC 19
30	30	GPIO AD B0_03	90	90	GPIO EMC 39
29	29	GPIO AD B0_14	89	89	GPIO EMC 40
28	28	GPIO AD B0_15	88	88	GPIO EMC 41
27	27		87	87	GPIO B0_00
26	26	USB OTG1_CHD	86	86	GPIO B0_01
25	25	USB OTG1_DP	85	85	GPIO B0_02
24	24	USB OTG1_DN	84	84	GPIO B0_03
23	23	USB OTG2_DP	83	83	GPIO B0_15
22	22	USB OTG2_DN	82	82	GPIO B1_00
21	21		81	81	GPIO B1_01
20	20		80	80	GPIO B1_02
19	19	GPIO AD B1_01	79	79	GPIO B1_03
18	18	GPIO AD B1_00	78	78	GPIO B0_09
17	17	CCM_CLK1_P	77	77	GPIO B0_10
16	16	CCM_CLK1_N	76	76	GPIO B0_11
15	15	GPIO AD B1_03	75	75	GPIO B0_12
14	14	GPIO AD B1_02	74	74	GPIO B0_13
13	13	PMIC_VSTBY_REQ	73	73	GPIO B0_14
12	12	RESET	72	72	GPIO B0_04
11	11	WAKEUP	71	71	GPIO B0_05
10	10	ONOFF	70	70	GPIO B0_06
9	9	GPIO SD B1_01	69	69	GPIO B0_07
8	8	GPIO SD B1_00	68	68	GPIO B0_08
7	7	GPIO SD B1_03	67	67	GPIO B1_11
6	6	GPIO SD B1_02	66	66	GPIO AD B0_04
5	5	GPIO SD B1_05	65	65	GPIO AD B0_02
4	4	GPIO SD B1_04	64	64	GPIO AD B0_01
3	3		63	63	GPIO AD B0_00
2	2	DCSV	62	62	GPIO B1_15
1	1		61	61	GPIO B1_14

HDR-1X34

HDR-1X34

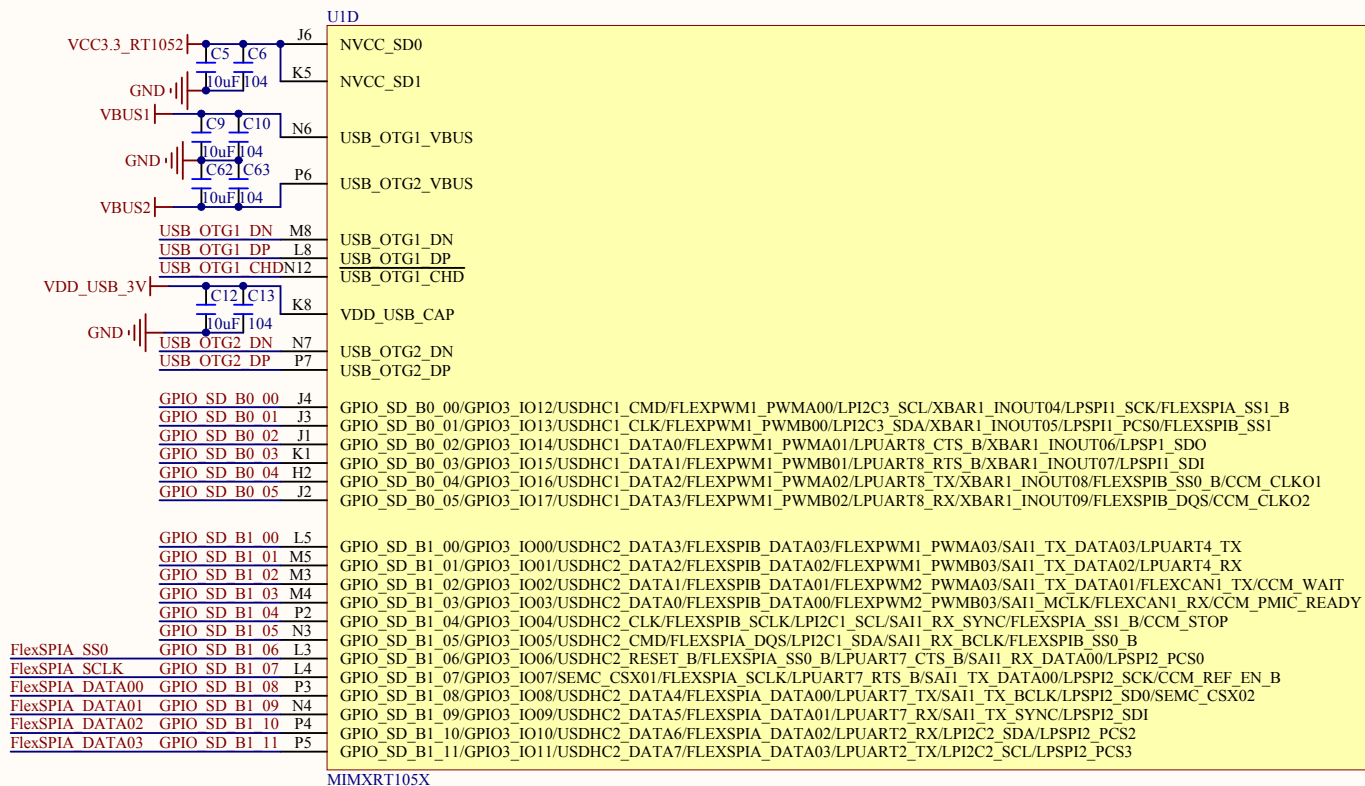
JP1			JP3		
60	60	GPIO B1_08	120	120	GND
59	59	GPIO B1_07	119	119	
58	58	GPIO B1_09	118	118	PMIC_ON_REQ
57	57	GPIO B1_10	117	117	GPIO SD B0_03
56	56	GPIO B1_06	116	116	GPIO SD B0_02
55	55	GPIO B1_05	115	115	GPIO SD B0_00
54	54	GPIO B1_04	114	114	GPIO SD B0_01
53	53	GPIO B1_13	113	113	GPIO SD B0_04
52	52	GPIO B1_12	112	112	GPIO SD B0_05
51	51	GPIO AD B0_06	111	111	GPIO EMC 00
50	50	GPIO AD B0_08	110	110	GPIO EMC 01
49	49	GPIO AD B0_09	109	109	GPIO EMC 02
48	48	GPIO AD B0_10	108	108	GPIO EMC 03
47	47	GPIO AD B0_11	107	107	GPIO EMC 04
46	46	GPIO AD B0_07	106	106	GPIO EMC 05
45	45	GPIO AD B0_05	105	105	GPIO EMC 06
44	44	GPIO AD B0_12	104	104	GPIO EMC 07
43	43	GPIO AD B0_13	103	103	GPIO EMC 30
42	42	GPIO AD B1_14	102	102	GPIO EMC 31
41	41	GPIO AD B1_12	101	101	GPIO EMC 32
40	40	GPIO AD B1_06	100	100	GPIO EMC 33
39	39	GPIO AD B1_05	99	99	GPIO EMC 34
38	38	GPIO AD B1_04	98	98	GPIO EMC 35
37	37	GPIO AD B1_10	97	97	GPIO EMC 36
36	36	GPIO AD B1_09	96	96	GPIO EMC 37
35	35	GPIO AD B1_08	95	95	

HDR-1X26

HDR-1X26

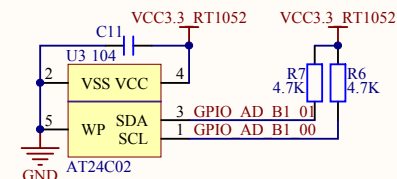
Title:		
LMX RT1052 Core Board		
Author:	Size:	
	A4	
Date:	File:	
2018-05-01	RT1052_CORE_GPIO2&PIN.SchDoc	
Revision:	Version:	
V1.4	V1.4	

GPIO3&USB

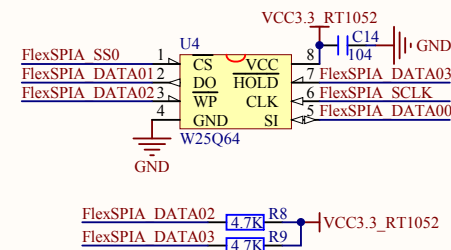


GPIO_SD_B1_05 (GPIO3_I005) 用作FLEXSPIA的DQS脚时:
不能用来做IO用! (请悬空).

24C02



QSPI FLASH



此QSPI FLASH用于存储代码或者只读数据, 不支持
读写, 所以如果需要做数据存储用, 请用SPI FLASH.

Title: LMX RT1052 Core Board		ALIENTEK
Author:	Size: A4	
Date: 2018-05-01	File: RT1052_CORE_GPIO3&MEMORY	
Revision: V1.4	Version: V1.4	

GPIO4&GPIO3



SEMC D0	GPIO EMC 00	E3
SEMC D1	GPIO EMC 01	F3
SEMC D2	GPIO EMC 02	F4
SEMC D3	GPIO EMC 03	G4
SEMC D4	GPIO EMC 04	F2
SEMC D5	GPIO EMC 05	G5
SEMC D6	GPIO EMC 06	H5
SEMC D7	GPIO EMC 07	H4
SEMC D8	GPIO EMC 31	C5
SEMC D9	GPIO EMC 32	D5
SEMC D10	GPIO EMC 33	C4
SEMC D11	GPIO EMC 34	D4
SEMC D12	GPIO EMC 35	E5
SEMC D13	GPIO EMC 36	C3
SEMC D14	GPIO EMC 37	E4

SEMC A0	GPIO EMC 09	C2
SEMC A1	GPIO EMC 10	G1
SEMC A2	GPIO EMC 11	G3
SEMC A3	GPIO EMC 12	H1
SEMC A4	GPIO EMC 13	A6
SEMC A5	GPIO EMC 14	B6
SEMC A6	GPIO EMC 15	B1
SEMC A7	GPIO EMC 16	A5
SEMC A8	GPIO EMC 17	A4
SEMC A9	GPIO EMC 18	B2
SEMC A10	GPIO EMC 23	G2
SEMC A11	GPIO EMC 19	B4
SEMC A12	GPIO EMC 20	A3

SEMC CAS	GPIO EMC 24	D3
SEMC RAS	GPIO EMC 25	D2
SEMC CLK	GPIO EMC 26	B3
SEMC CKE	GPIO EMC 27	A2
SEMC WE	GPIO EMC 28	D1
SEMC CS0	GPIO EMC 29	E1
SEMC CS1	GPIO EMC 39	B7

SEMC BA0	GPIO EMC 21	C1
SEMC BA1	GPIO EMC 22	F1

SEMC DM00	GPIO EMC 08	H3
SEMC DM01	GPIO EMC 38	D6

SEMC RDY	GPIO EMC 40	A7
SEMC CSX00	GPIO EMC 41	C7

U1C

NVCC_EMC_1
NVCC_EMC_2

GPIO_EMC_00/GPIO4_I000/SEMC_DATA00/FLEXPWM4_PWMA00/LPSPI2_SCK/XBAR1_XBAR_IN02/FLEXIO1_FLEXIO00/GPIO4_I000/JTAG_DONE
GPIO_EMC_01/GPIO4_I001/SEMC_DATA01/FLEXPWM4_PWMB00/LPSPI2_PCS0/XBAR1_IN03/FLEXIO1_FLEXIO01/GPIO4_I001/JTAG_DE_B
GPIO_EMC_02/GPIO4_I002/SEMC_DATA02/FLEXPWM4_PWMA01/LPSPI2_SDO/XBAR1_INOUT04/FLEXIO1_FLEXIO02/JTAG_FAIL
GPIO_EMC_03/GPIO4_I003/SEMC_DATA03/FLEXPWM4_PWMB01/LPSPI2_SDI/XBAR1_INOUT05/FLEXIO1_FLEXIO03/JTAG_ACTIVE
GPIO_EMC_04/GPIO4_I004/SEMC_DATA04/FLEXPWM4_PWMA02/SAI2_TX_DATA/XBAR1_INOUT06/FLEXIO1_FLEXIO04
GPIO_EMC_05/GPIO4_I005/SEMC_DATA05/FLEXPWM4_PWMB02/SAI2_TX_SYNC/XBAR1_INOUT07/FLEXIO1_FLEXIO05
GPIO_EMC_06/GPIO4_I006/SEMC_DATA06/FLEXPWM2_PWMA00/SAI2_TX_BCLK/XBAR1_INOUT08/FLEXIO1_FLEXIO06
GPIO_EMC_07/GPIO4_I007/SEMC_DATA07/FLEXPWM2_PWMB00/SAI2_MCLK/XBAR1_INOUT09/FLEXIO1_FLEXIO07
GPIO_EMC_30/GPIO4_I030/SEMC_DATA08/FLEXPWM3_PWMB00/LPUART6_CTS_B/LPSPI1_PCS0/CSI_DATA23
GPIO_EMC_31/GPIO4_I031/SEMC_DATA09/FLEXPWM3_PWMA01/LPUART7_TX/LPSPI1_PCS1/CSI_DATA22
GPIO_EMC_32/GPIO3_I018/SEMC_DATA10/FLEXPWM3_PWMB01/LPUART7_RX/CCM_PMIC_RDY/CSI_DATA21
GPIO_EMC_33/GPIO3_I019/SEMC_DATA11/FLEXPWM3_PWMA02/USDHC1_RESET_B/SAI3_RX_DATA/CSI_DATA20
GPIO_EMC_34/GPIO3_I020/SEMC_DATA12/FLEXPWM3_PWMB02/USDHC1_VSELECT/SAI3_RX_SYNC/CSI_DATA19
GPIO_EMC_35/GPIO3_I021/SEMC_DATA13/XBAR1_INOUT18/GPT1_COMPARE1/SAI3_RX_BCLK/CSI_DATA18/USDHC1_CD_B
GPIO_EMC_36/GPIO3_I022/SEMC_DATA14/XBAR1_IN22/GPT1_COMPARE2/SAI3_TX_DATA/CSI_DATA17/USDHC1_WP
GPIO_EMC_37/GPIO3_I023/SEMC_DATA15/XBAR1_IN23/GPT1_COMPARE3/SAI3_MCLK/CSI_DATA16/USDHC2_WP

GPIO_EMC_09/GPIO4_I009/SEMC_ADDR00/FLEXPWM2_PWMB01/SAI2_RX_SYNC/FLEXCAN2_TX/FLEXIO1_FLEXIO09
GPIO_EMC_10/GPIO4_I010/SEMC_ADDR01/FLEXPWM2_PWMA02/SAI2_RX_BCLK/FLEXCAN2_RX/FLEXIO1_FLEXIO10
GPIO_EMC_11/GPIO4_I011/SEMC_ADDR02/FLEXPWM2_PWMB02/LP12C4_SDA/USDHC2_RESET_B/FLEXIO1_FLEXIO11
GPIO_EMC_12/GPIO4_I012/SEMC_ADDR03/XBAR1_IN24/LP12C4_SCL/USDHC1_WP/FLEXPWM1_PWMA03
GPIO_EMC_13/GPIO4_I013/SEMC_ADDR04/XBAR1_IN25/LPUART3_TX/MQS_RIGHT/FLEXPWM1_PWMB03
GPIO_EMC_14/GPIO4_I014/SEMC_ADDR05/XBAR1_INOUT19/LPUART3_RX/MQS_LEFT/LPSPI2_PCS1
GPIO_EMC_15/GPIO4_I015/SEMC_ADDR06/XBAR1_IN20/LPUART3_CTS_B/SPDIF_OUT/QTIMER3_TIMER0
GPIO_EMC_16/GPIO4_I016/SEMC_ADDR07/XBAR1_IN21/LPUART3_RTS_B/SPDIF_IN/QTIMER3_TIMER1
GPIO_EMC_17/GPIO4_I017/SEMC_ADDR08/FLEXPWM4_PWMA03/LPUART4_CTS_B/FLEXCAN1_TX/QTIMER3_TIMER2
GPIO_EMC_18/GPIO4_I018/SEMC_ADDR09/FLEXPWM4_PWMB03/LPUART4_RTS_B/FLEXCAN1_RX/QTIMER3_TIMER3/SNVS_VIO_5_CTL
GPIO_EMC_23/GPIO4_I023/SEMC_ADDR10/FLEXPWM1_PWMA00/LPUART5_TX/ENET_RX_EN/GPT1_CAPTURE2
GPIO_EMC_19/GPIO4_I019/SEMC_ADDR11/FLEXPWM2_PWMA03/LPUART4_TX/ENET_RDATA01/QTIMER2_TIMER0/SNVS_VIO_5
GPIO_EMC_20/GPIO4_I020/SEMC_ADDR12/FLEXPWM2_PWMB03/LPUART4_RX/ENET_RDATA00/QTIMER2_TIMER1

GPIO_EMC_24/GPIO4_I024/SEMC_CAS/FLEXPWM1_PWMB00/LPUART5_RX/ENET_TX_EN/GPT1_CAPTURE1
GPIO_EMC_25/GPIO4_I025/SEMC_RAS/FLEXPWM1_PWMA01/LPUART6_TX/ENET_TX_CLK/ENET_REF_CLK
GPIO_EMC_26/GPIO4_I026/SEMC_CLK/FLEXPWM1_PWMB01/LPUART6_RX/ENET_RX_ER/FLEXIO1_FLEXIO12
GPIO_EMC_27/GPIO4_I027/SEMC_CKE/FLEXPWM1_PWMA02/LPUART5_RTS_B/LPSPI1_SCK/FLEXIO1_FLEXIO13
GPIO_EMC_28/GPIO4_I028/SEMC_WE/FLEXPWM1_PWMB02/LPUART5_CTS_B/LPSPI1_SDO/FLEXIO1_FLEXIO14
GPIO_EMC_29/GPIO4_I029/SEMC_CS0/FLEXPWM3_PWMA00/LPUART6_RTS_B/LPSPI1_SDI/FLEXIO1_FLEXIO15
GPIO_EMC_39/GPIO3_I025/SEMC_DQS/FLEXPWM1_PWMB03/LPUART8_RX/SAI3_TX_SYNC/WDOG1_WDOG_B/USDHC2_CD_B

GPIO_EMC_21/GPIO4_I021/SEMC_BA0/FLEXPWM3_PWMA03/LP12C3_SDA/ENET_TDATA01/QTIMER2_TIMER2
GPIO_EMC_22/GPIO4_I022/SEMC_BA1/FLEXPWM3_PWMB03/LP12C3_SCL/ENET_TDATA00/QTIMER2_TIMER3

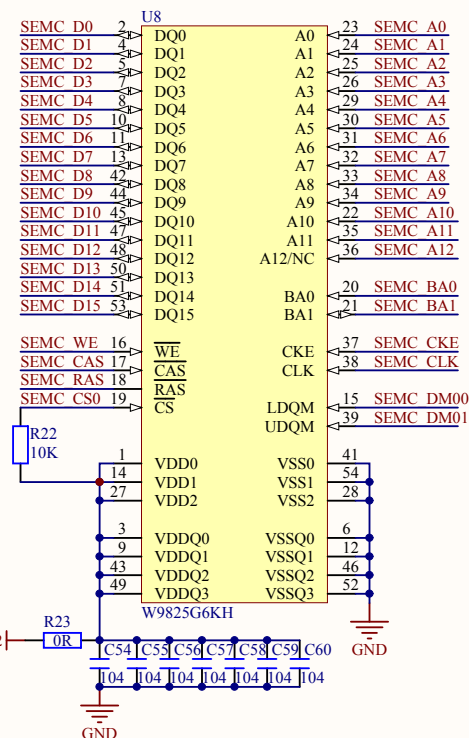
GPIO_EMC_08/GPIO4_I008/SEMC_DM00/FLEXPWM2_PWMA01/SAI2_RX_DATA/XBAR1_INOUT17/FLEXIO1_FLEXIO08
GPIO_EMC_38/GPIO3_I024/SEMC_DM01/FLEXPWM1_PWMA03/LPUART8_TX/SAI3_TX_BCLK/CSI_FIELD/USDHC2_VSELECT

GPIO_EMC_40/GPIO3_I026/SEMC_RDY/GPT2_CAPTURE2/LPSPI1_PCS2/USB_OTG2_OC/ENET_MDC/USDHC2_RESET_B
GPIO_EMC_41/GPIO3_I027/SEMC_CSX00/GPT2_CAPTURE1/LPSPI1_PCS3/USB_OTG2_PWR/ENET_MDIO/USDHC1_VSELECT

MIMXRT105X

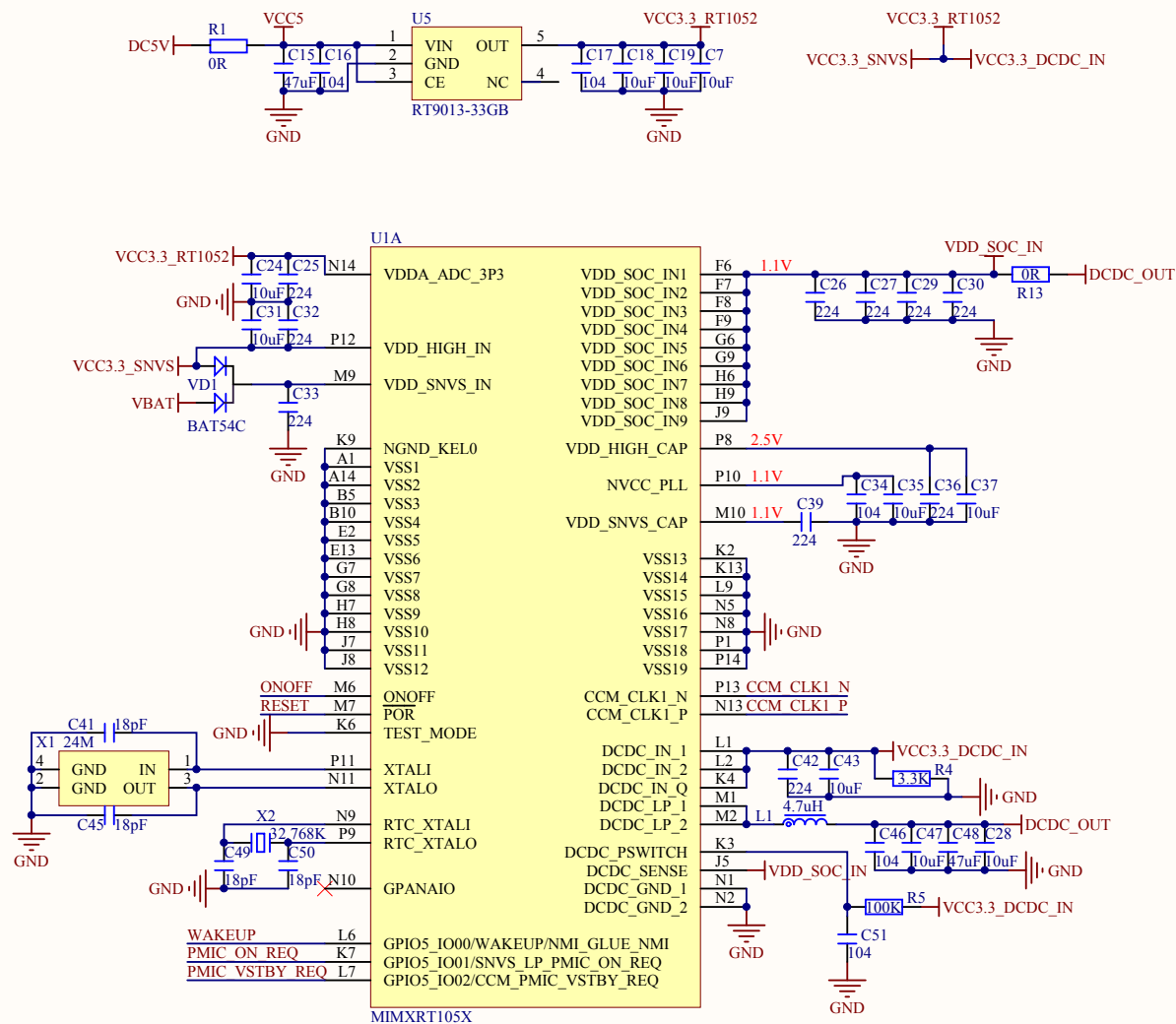
GPIO_EMC_39(GPIO3_I025)用作SDRAM的DQS脚时:
不能用来做IO用!(请悬空),且C61不要焊接.
如果SDRAM不需要跑>133M,只要跑≤133M左右,
则DQS脚可以用作普通IO使用(无需设置SDRAM_DQS).

SDRAM



Title:		
LMX RT1052 Core Board		
Author:	Size:	
	A4	
Date:	File:	SchDoc
2018-05-01	RT1052_CORE_GPIO4&SDRAM.SchDoc	
Revision:	Version:	
V1.4	V1.4	

POWER&GPIO5



Title: IMX RT1052 Core Board		ALIENTEK
Author:	Size: A4	
Date: 2018-05-01	File: RT1052_CORE_GPIO5&POWER.SchDoc	
Revision: V1.4	Version: V1.4	

39.00mm

30.00mm

